PATENT APPLICATION

METHOD AND APPARATUS FOR VOICE TRANSCODING BETWEEN VARIABLE RATE CODERS

Inventors: Marwan A. Jabri, a citizen of Australia, residing at
Level 7, 3 Smail Street
Broadway, NSW, 2007 Australia

Jianwei Wang, a citizen of Australia, residing at
104 Killarney Drive
Killarney Heights, NSW, 2087 Australia

Nicola Chong-White, a citizen of New Zealand, residing at
319 Greenwich Road
Greenwich, NSW, 2065 Australia

Assignee: Dilithium Holdings, Inc.
700 Larkspur Landing Circle, Suite 263,
Larkspur, CA, 94939

Entity: Small business concern

METHOD AND APPARATUS FOR VOICE TRANSCODING
BETWEEN VARIABLE RATE CODERS

BACKGROUND OF THE INVENTION

5   [0001]   The present invention relates generally to processing of telecommunication signals. More particularly, the present invention relates to a method and apparatus for transcoding a bitstream encoded by a first voice speech coding format into a bitstream encoded by a second variable-rate voice coding format. Merely by way of example, the invention has been applied to variable-rate voice transcoding, but it would be recognized that the invention may also be

10   applicable to other applications.

[0002]   Telecommunication techniques have progressed through the years. One of the major desires of speech coding development is high quality output speech at a low average data rate. One approach is to employ a variable bit-rate scheme, whereby the transmission rate is not only determined by the network traffic but also from the characteristics of the input

15   speech signal. For example, when the signal is highly voiced, a high bit rate may be chosen; if the signal is weak, a low bit rate is chosen; and if the signal has mostly silence or background noise, a lower bit rate is chosen. This often provides efficient allocation of the available bandwidth, without sacrificing output voice quality. Such variable-rate coders include the TIA IS-127 Enhanced Variable Rate Codec (EVRC), and 3rd generation

20   partnership project 2 (3GPP2) Selectable Mode Vocoder (SMV). These coders use Rate Set 1 of the Code Division Multiple Access (CDMA) communication standards IS-95 and cdma2000, which include rates of 8.55 kbit/s (Rate 1 or full Rate), 4.0 kbit/s (half-rate), 2.0 kbit/s (quarter-rate) and 0.8 kbit/s (eighth rate). SMV selects the bit rate based on the input speech characteristics and operates in one of six network controlled modes, which limit the

25   bit rate during high traffic. Depending on the mode of operation, different thresholds may be set to determine the rate usage percentages.

[0003]   To accurately decide the desired transmission rate, and obtain high quality output speech at that rate, input speech frames are categorized into various classes. For example, in SMV, these classes include silence, unvoiced, onset, plosive, non-stationary voiced and

30   stationary voiced speech. It is known that certain coding techniques are better suited for certain classes of sounds. Also, some types of sounds, for example, voice onsets or

unvoiced-to-voiced transition regions, have higher perceptual significance and thus generally require higher coding accuracy than other classes of sounds, such as unvoiced speech. Thus, the speech frame classification may be used, not only to decide the most efficient transmission rate, but also the best-suited coding algorithm.

5   [0004]   Accurate classification of input speech frames is desired to fully exploit the signal redundancies and perceptual importance. Typical frame classification techniques include voice activity detection, measuring the amount of noise in the signal, measuring the level of voicing, detecting speech onsets, and measuring the energy in a number of frequency bands. These measures generally require the calculation of numerous parameters, such as maximum

10   correlation values, line spectral frequencies, and frequency transformations.

[0005]   While coders such as SMV achieve much better quality at lower average data rate than existing speech codecs at similar bit rates, the frame classification and rate determination algorithms are complex. In the case of a tandem connection of two speech vocoders, however, many of the measurements performed for frame classification have already been

15   calculated in the source codec. This can be capitalized on in a transcoding framework. In transcoding from the bitstream format of one CELP codec to the bitstream format of another CELP codec, rather than fully decoding to PCM and re-encoding the speech signal, smart interpolation methods may be applied directly in the CELP parameter space. Hence the parameters, such as pitch lag, pitch gain, fixed codebook gain, line spectral frequencies and

20   the source codec bit rate are available to the destination codec. This allows frame classification and rate determination of the destination voice codec to be performed in a fast manner.

[0006]   The simplest method of transcoding is a brute-force approach called tandem transcoding, shown in Figure 1. This method performs a full decode of the incoming

25   compressed bits to produce synthesized speech. The synthesized speech is then encoded for the target standard. This method is undesirable because of the huge amount of computation performed in re-encoding the signal, as well as quality degradations introduced by pre- and post-filtering of the speech waveform, and the potential delays introduced by the look-ahead-requirements of the encoder.

30   [0007]   Methods for "smart" transcoding similar to that illustrated in Figure 2 have appeared in the literature. These methods essentially reconstruct the speech signal and then perform significant work to extract the various CELP parameters such as line spectral

2

frequencies and pitch. That is, these methods still operate in the speech signal space. In particular, the excitation signal which has already been optimally matched to the original speech by the far-end source encoder (encoder that produced the compressed speech according to a compression format) is often only used for the generation of the synthesized speech. The synthesized speech is then used to compute a new optimal excitation. Due to the requirement of incorporating impulse response filtering operations in closed-loop searches of the excitation parameters, this becomes a very computationally intensive operation.

[0008] Further, these transcoding methods do not cover the transcoding between variable-rate voice coders which determine the bit rate based on the characteristics of the input speech and, in some cases, external commands. During the transcoding process, the frame classification and rate decision of the destination voice codec in transcoding are still computed through the speech signal domain. The transcoder thus includes the equivalent amount of computational resources as the destination codec to classify frame types and to determine the bit rates. The smart transcoding of previous methods may lose part of their computational advantage, as the classification algorithms require parameters from intermediate stages of functions that have been omitted. For example, recalculation of the line spectral frequencies is often not performed in transcoding, however, the LPC prediction gain, LPC prediction error, autocorrelation function and reflection coefficients are often required in the classification and rate determination process.

[0009] From the above, it is seen that improved telecommunication techniques are desired.

BRIEF SUMMARY OF THE INVENTION

[0010] According to the present invention, techniques for processing of telecommunication signals are provided. More particularly, the present invention relates to a method and apparatus for transcoding a bitstream encoded by a first voice speech coding format into a bitstream encoded by a second variable-rate voice coding format. Merely by way of example, the invention has been applied to variable-rate voice transcoding, but it would be recognized that the invention may also be applicable to other applications.

[0011] According to an aspect of the present invention, there is provided a voice transcoding apparatus comprising:

- a first voice compression code parameter unpack module that extracts the input encoded bitstream according to the first voice codec standard into its speech parameters. In the case of CELP-based codecs, these parameters may be line spectral frequencies, pitch lag, adaptive codebook gains, fixed codebook gains, codevectors as well as other parameters;

- a frame classification and rate determination module that takes the parameters from the input encoded bitstream and external control commands to generate the destination codec frame type and rate decision;

- at least one parameter interpolator and mapping module that converts the input source parameters into destination encoded parameters, taking into account the subframe and/or frame size difference between the source and destination codec.

- a destination parameter packer that converts the encoded parameters into output encoded packets;

- a first stage switching module that connects the source parameter unpack module to a parameter interpolator and mapping module;

- a second stage switching module that connects the destination parameter pack module to a parameter interpolator and mapping module;

- a control engine that controls the selection of parameter tuning engine to adapt the available resource and signal processing requirement;

- a status reporting module that provides the status of parameter-based transcoding.

[0012] Numerous benefits are achieved using the present invention over conventional techniques. These benefits have been listed below:

To perform smart voice transcoding between variable-rate voice codecs;

To classify the destination codec frame type directly from the parameters of input source codec frames;

To determine the rate of the destination codec directly from the parameters of input source codec frames;

To improve voice quality through mapping parameters in the parameter space;

4

To reduce the computational complexity of the transcoding process;

To reduce the delay through the transcoding process;

To reduce the amount of memory required by the transcoding; and

To provide a generic transcoding architecture that may be adapted to current and future variable-rate codecs.

[0013]   Depending upon the embodiment, one or more of these benefits may be achieved. These and other benefits are described throughout the present specification and more particularly below.

[0014]   Other features and advantages of the present invention will be apparent from the following description taken in conjunction with the accompanying drawing, in which like reference characters designate the same or similar parts throughout the figures thereof.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015]   The objectives, features, and advantages of the present invention, which are believed to be novel, are set forth in detail in the appended claims. The present invention, both as to its organization and manner of operation, together with further objectives and advantages, may best be understood by reference to the following description, in connection with the accompanying drawings.

[0016]   Figure 1 is a simplified block diagram illustrating the general tandem coding connection to convert a bitstream from one codec format to another codec format;

[0017]   Figure 2 is a simplified block diagram illustrating a general transcoder connection to convert a bitstream from one codec format to another codec format without full decode and re-encode.

[0018]   Figure 3 is a simplified block diagram illustrating the encoding processes performed in a variable-rate voice encoder.

[0019]   Figure 4 is a simplified block diagram of the variable-rate voice codec transcoding according to an embodiment of the present invention based on a smart frame classification and rate determination method.

[0020] Figure 5 is a simplified flowchart of the steps performed in the variable-rate voice codec transcoding according to an embodiment of the present invention based on a smart frame classification and rate determination method

[0021] Figure 6 is a simplified diagram of a smart frame classification and rate determination classifier according to an embodiment of the present invention.

[0022] Figure 7 is a simplified block diagram illustrating the frame classification and rate determination in a variable-rate encoder according to an embodiment of the present invention.

[0023] Figure 8 illustrates the various stages of frame classification in a variable-rate voice encoder according to an embodiment of the present invention.

[0024] Figure 9 is a simplified block diagram illustrating a first set of CELP parameters for an active frame being transformed to a second set of CELP parameters according to an embodiment of the present invention.

[0025] Figure 10 is a simplified block diagram illustrating a first set of CELP parameters for a silence or noise-like frame being transformed to a second set of CELP parameters according to an embodiment of the present invention.

[0026] Figure 11 is a simplified block diagram illustrating the decoding process performed in a RCELP-based voice decoder according to an embodiment of the present invention.

[0027] Figure 12 illustrates the various stages of voice signal pre-processing in a variable rate voice encoder according to an embodiment of the present invention.

[0028] Figure 13 is a simplified block diagram illustrating the subframe excitation encoding process performed in a RCELP-based voice encoder according to an embodiment of the present invention.

[0029] Figure 14 is a simplified block diagram illustrating the subframe excitation encoding process performed in another RCELP-based voice encoder according to an embodiment of the present invention.

[0030] Figure 15 is a simplified block diagram illustrating an embodiment of the subframe excitation transcoding process according to the present invention according to an embodiment of the present invention.

[0031] Figure 16 is a simplified flowchart showing the steps of an embodiment of the subframe excitation transcoding process according to an embodiment of the present invention.

[0032] Figure 17 is a simplified block diagram illustrating the voice transcoding procedure from EVRC to SMV according to an embodiment of the present invention.

[0033] Figure 18 is a simplified block diagram illustrating the voice transcoding procedure from SMV to EVRC according to an embodiment of the present invention.

[0034] Figure 19 is a simplified diagram illustrating the subframe size and frame size of different frame types and different rates in the SMV voice coder according to an embodiment of the present invention.

[0035] Figure 20 is a simplified diagram illustrating the subframe size and frame size of different rates in the EVRC voice coder according to an embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0036] According to the present invention, techniques for processing of telecommunication signals are provided. More particularly, the present invention relates to a method and apparatus for transcoding a bitstream encoded by a first voice speech coding format into a bitstream encoded by a second variable-rate voice coding format. Merely by way of example, the invention has been applied to variable-rate voice transcoding, but it would be recognized that the invention may also be applicable to other applications.

[0037] A method and apparatus of the invention are discussed in detail below. In the following description, for purposes of explanation, numerous specific details are set forth in order to provide a thorough understanding of the present invention. The case of SMV and EVRC are used for the purpose of illustration and for examples. The methods described here are generic and apply to the transcoding between any pair of linear prediction-based voice codecs. A person skilled in the relevant art will recognize that other steps, configurations and arrangements can be used without departing from the spirit and scope of the present invention.

[0038] A block diagram of a tandem connection between two voice codecs is shown in Figure 1. Alternatively a transcoder may be used, as shown in Figure 2, which converts the

7

bitstream from a source codec to the bitstream of a destination codec without fully decoding the signal to PCM and then re-encoding the signal. The present invention is a transcoder between voice codecs, whereby the destination codec is a variable bit-rate voice codec that determines the bit-rate based on the input speech characteristics. A block diagram of the

5   encoder of a variable bit-rate voice coder is shown in Figure 3. The input speech signal passes through several processing stages including pre-processing, estimation of model parameters and computation of classification features. Then, a rate, and in some cases, a frame type, is determined based on the features detected. Depending on the rate decision, a different strategy may be used in the encoding process. Once coding is complete, the

10   parameters are packed in the bitstream.

[0039]   A diagram of the apparatus for transcoding between two variable bit-rate voce codecs of the present invention is shown in Figure 4. The apparatus comprises a source codec unpacking module, an intermediate parameters interpolation module, a smart frame classification and rate determination module, several mapping strategy modules, a switching

15   module to select the desired mapping strategy, a destination packet formation module, and a second switching module that links the mapping strategy to the destination packet formation module. The method for transcoding between two variable bit-rate voce codecs is shown in Figure 5.

[0040]   Firstly, the bitstream representing frames of data encoded according to the source

20   voice codec is unpacked and unquantized by a bitstream unpacking module. The actual parameters extracted from the bitstream depend on the source codec and its bit rate, and may include line spectral frequencies, pitch delays, delta pitch delays, adaptive codebook gains, fixed codebook shapes, fixed codebook gains and frame energy. Particular voice codecs may also transmit information regarding spectral transition, interpolation factors, the switch

25   predictor used as well as other minor parameters. The unquantised parameters are passed to the intermediate parameters interpolation module.

[0041]   The intermediate parameters interpolation module interpolates between different frame sizes, subframe sizes and sampling rates. This is required if there are differences in the frame size or subframe size of the source and destination codecs, in which case the

30   transmission frequency of parameters may not be matched. Also, a difference in the sampling rate between the source codec and destination codec requires modification of

parameters. The output interpolated parameters are passed to the smart frame classification and rate determination module and one of the mapping modules.

[0042] The frame classification and rate determination module receives the unquantized interpolated parameters of the source codec and the external control commands of the destination codec, as shown in Figure 6. The frame classification and rate determination module comprises a classifier input parameter selector, for selecting which inputs will be used in the classification task, M sub-classifiers, buffers to store past input parameters and past output values, and a final decision module. The classifier takes as input the selected classification input parameters, external commands, and past input and output values, and generates as output the frame class and rate decision for the destination codec. Once classification has been performed, the states of the data buffers storing past parameter values are updated. The output rate and frame type decision controls the first switching module that selects the parameter mapping module, and the second switching module that links the parameter mapping module to the bitstream packing module. frame classification is performed according to pre-defined coefficients or rules determined during a prior training or classifier construction process. Several types of classification techniques may be used, including but not exclusive to, decision trees, rule-based models, and artificial neural networks. The functions for computing classification features and the many steps of the classification procedure for a particular codec are shown in Figure 7 and Figure 8 respectively. In an embodiment of the present invention, the frame classification and rate determination module replaces the standard classifier of the destination codec, as well as the processing functions of the destination codec required to generate the classification parameters.

[0043] The intermediate parameters interpolation module and the frame classification and rate determination module are linked to one of many parameter mapping modules by a switching module. The destination codec frame type and bit rate determined by the frame classification and rate determination module control which mapping module is to be chosen. Mapping modules may exist for each combination of bit-rate and frame class of the source codec to each bit rate and frame class of the destination codec.

[0044] Each mapping module comprises a speech spectral parameter mapping unit, an excitation mapping unit, and a mapping strategy decision unit. The speech spectral parameter mapping unit maps the spectral parameters, usually line spectral pairs (LSPs) or line spectral

frequencies (LSFs), of the source codec, directly to the spectral parameters of the destination codec. A calibration factor is calculated and used to calibrate the excitation to account for the differences in the quantised spectral parameters of the source and destination codec. The excitation mapping unit takes CELP excitation parameters including pitch lag, adaptive

5   codebook gain, fixed codebook gain and fixed codebook codevectors from the interpolator and maps these to encoded CELP excitation parameters according to the destination codec. Figure 9 shows a mapping module which may be selected for mapping parameters of an active speech frame, e.g., mapping from Rate ½ or Rate 1 of EVRC to Rate ½ or Rate 1 of SMV.   In this case, the input parameters to the excitation coding mapping unit are the

10   adaptive codebook lag, adaptive codebook gain, fixed codebook codevector and fixed codebook gain of the source codec. The output parameters to the excitation coding mapping unit are the adaptive codebook lag, adaptive codebook gain, fixed codebook codevector and fixed codebook gain in the format of the destination codec. Figure 10 shows a mapping module which may be selected for mapping parameters of a silence or noise-like speech

15   frame, e.g., mapping from Rate 1/8 of EVRC to Rate ¼ or Rate 1/8 of SMV. In this case, the input parameters to the excitation coding mapping unit are typically the frame energy or subframe energies, and excitation shape. Not all excitation parameters shown in the figures may be present for a given codec or bit rate.

[0045]   Linked to the excitation coding mapping unit is a mapping strategy decision unit,

20   which controls the type of excitation mapping to be used. Several mapping approaches may be used, including those using direct mapping from source codec to destination codec without any further analysis or iterations, analysis in the excitation domain, analysis in the filtered excitation domain or a combination of these strategies, such as searching the adaptive codebook in the excitation space and fixed codebook in the filtered excitation space. The

25   mapping strategy decision module determines which mapping strategy is to be applied. The decision may be based on available computational resources or minimum quality requirements and can change in a dynamic fashion.

[0046]   Except for the direct mapping strategy, in which parameters are directly mapped from source codec format to destination codec format without any analysis, the excitation

30   signal is reconstructed. Reconstruction of the excitation during active speech requires the interpolated excitation parameters of pitch delays, adaptive codebook gains, fixed codebook shapes, and fixed codebook gains. During silence or noise, the parameters required are the signal energy, signal shape if available, and a random noise generator. Figure 11 shows a

block diagram the decoding process performed in a RCELP-based voice decoder. In this figure, the linear prediction (LP) excitation is formed by combining the gain-scaled contributions of the adaptive and fixed codebooks, and then filtered by the speech synthesis filter and post-filter. In the transcoder architecture of the present invention, to reduce

5   complexity and quality degradations, the final source codec decoder operations of filtering the LP excitation signal by the synthesis filter to convert to the speech domain and then post-filtering to mask quantization noise are not used. Similarly, the pre-processing operations in the encoder of the destination codec are not used. An example of a speech pre-processor is shown in Figure 12. High-pass filtering is a common pre-processing step in existing CELP-

10   based voice codecs, with the advanced steps of silence enhancement, noise suppression and adaptive tilt filtering being applied in more recent voice codecs. In the case where the source codec does not use noise suppression and the destination codec does use noise suppression, the transcoder architecture should provide noise suppression functionality.

[0047]   Current variable-rate voice codecs applicable to the present invention include

15   EVRC and SMV which are based on the Relaxed CELP (RCELP) principle. Typical excitation quantization in RCELP codecs is performed by the technique shown in Figure 13 and Figure 14. In this case, the target signal is modified weighted speech. The modification is performed to create a signal with a smooth interpolated pitch delay contour by time-warping or time-shifting pitch pulses. This allows for coarse pitch quantization. The

20   adaptive codebook is mapped to the delay contour and then searched by gain-adjusting and filtering each candidate vector by the weighted synthesis filter and comparing the result to the target signal. Once the best adaptive codebook vector is found, its contribution is subtracted from the target, and the fixed codebook is searched in a similar manner. In the case where both source and destination codecs are based on the RCELP principle, the computationally

25   expensive operation of detecting and shifting each pitch pulse in the encoder processing of the destination codec is not required. This is due to the fact that the reconstructed source excitation already follows the interpolated pitch track of the source codec. Hence, the target signal in the transcoder is not modified weighted speech, but simply the weighted speech, speech, weighted excitation, excitation, or calibrated excitation signal.

30   [0048]   Figure 15 shows a block diagram of an example of one mapping strategy of the transcoder between variable-rate voice codecs of the present invention. The procedure is outlined in Figure 16. In this case, the mapping strategy chosen is a combination between analysis in the excitation domain and analysis in the filtered excitation domain. The target

signal for the adaptive codebook search is the calibrated excitation signal. The search of the adaptive codebook is performed in the excitation domain. This reduces complexity as each candidate codevector does not need to be filtered with the weighted synthesis filter before it can be compared to a speech domain target signal. The initial estimate of the pitch lag is the

5   pitch lag obtained from the interpolation module that has been interpolated to match the subframe size of the destination codec. The pitch is searched within a small interval of the initial pitch estimate, at the accuracy (integer or fractional pitch) required by the destination codec. The adaptive codebook gain is then determined for the best codevector and the adaptive codevector contribution is removed from the calibrated excitation. The result is

10   filtered using a special weighting filter to produce the target signal for the fixed codebook search. The fixed codebook is then searched, either by a fast technique or by gain-adjusting and filtering candidate codevectors by the special weighting filter and comparing the result with the target. Fast search methods may be applied for both the adaptive and fixed codebook searches.

15   [0049]   Another mapping strategy is to perform both the adaptive codebook and fixed codebook searches in the excitation domain. A further mapping strategy is to perform both the adaptive codebook and fixed codebook searches in the filtered excitation domain. Alternatively, parameters may be directly mapped from source to destination codec format without any searching. It is noted that any combinations of the above strategies may also be

20   used. The best strategy in terms of both high quality and low complexity will depend on the source and destination codecs and bit rates.

[0050]   A second-stage switching module links the interpolation and mapping module to the destination bitstream packing module. The destination bitstream packing module packs the destination CELP parameters in accordance with the destination codec standard. The

25   parameters to be packed depend on the destination codec, the bit rate and frame type.

EVRC ⇔ SMV TRANSCODING EXAMPLE

[0051]   As an example, it is assumed that the source codec is the Enhanced Variable Rate Codec (EVRC) and the destination codec is the Selectable Mode Vocoder (SMV).

[0052]   EVRC and SMV are both variable-rate codecs that determine the bit rate based on

30   the characteristics of the input speech. These coders use Rate Set 1 of the Code Division Multiple Access communication standards IS-95 and cdma2000, which consists of the rates 8.55 kbit/s (Rate 1 or full Rate), 4.0 kbit/s (Rate ½ or half-rate), 2.0 kbit/s (Rate ¼ or quarter-

12

rate) and 0.8 kbit/s (Rate 1/8 or eighth rate). EVRC uses Rate 1, Rate ½, and Rate 1/8; it does not use quarter-rate. SMV uses all four rates and also operates in one of six network controlled modes, Modes 0 to 6, which limits the bit rate during high traffic. Modes 4 and 5 are half-rate maximum modes. Depending on the mode of operation, different thresholds

5   may be set to determine the rate usage percentages.

[0053]   A diagram of the apparatus for transcoding from EVRC to SMV is shown in Figure 17. The apparatus comprises an EVRC unpacking module, an intermediate parameters interpolation module, a smart SMV frame classification and rate determination module, several mapping modules to map parameters from all allowed rate and type transcoder

10   transitions, and a SMV packet formation module. The inputs to the apparatus are the EVRC frame packets and SMV external commands (e.g. network-controlled mode, half-rate max flag), and the outputs are the SMV frame packets. Similarly, the apparatus for transcoding from SMV to EVRC is shown in Figure 18. The apparatus comprises a SMV unpacking module, an intermediate parameters interpolation module, an EVRC rate determination

15   module, several mapping modules to map parameters from all allowed rate and type transcoder transitions, and an EVRC packet formation module. The inputs to the apparatus are the SMV frame packets and EVRC external commands (e.g. half-rate max flag), and the outputs are the EVRC frame packets.

[0054]   In transcoding from EVRC to SMV, the bitstream representing frames of data

20   encoded according to EVRC is unpacked by a bitstream unpacking module. The actual parameters from the bitstream depend on the EVRC bit rate and include line spectral frequencies, spectral transition indicator, pitch delay, delta pitch delay, adaptive codebook gain, fixed codebook shapes, fixed codebook gains and frame energy. The unquantised parameters are passed to the intermediate parameters interpolation module.

25   [0055]   The intermediate parameter interpolation module interpolates between the different subframe sizes of EVRC and SMV. EVRC has 3 subframes per frame, whereas SMV has 1, 2, 3, 4, or 10 subframes per frame depending on the bit rate and frame type. Depending on the parameter and coding strategy, subframe interpolation may or may not be required. Figure 19 and Figure 20 illustrate the frame and subframe sizes for the different rates and frame

30   types of SMV and EVRC respectively. Since the frame size of both codecs is 20ms and the sampling rate of both codecs is 8kHz, no frame size or sampling rate interpolation is required. The output interpolated parameters, or if no interpolation was carried out, the EVRC CELP

parameters, are passed to the smart frame classification and rate determination module and the selected of the mapping module.

[0056] The frame classification and rate determination module receives the EVRC CELP parameters, the EVRC bit rate, the SMV network-controlled mode and any other SMV external commands. The frame classification and rate determination module produces a frame class and rate decision for SMV based on these inputs. The frame classification and rate determination module comprises a classifier input parameter selector, for selecting which of the EVRC parameters will be used as inputs to the classification task, M sub-classifiers, buffers to store past input parameters and past output values and a final decision module. The sub-classifiers take as input the selected classification input parameters, the SMV network-controlled mode command, and past input and output values, and generate the frame class and rate decision. One sub-classifier may be used to determine the bit rate, and a second sub-classifier may be used to determine the frame class. The SMV frame class is either silence, noise-like, unvoiced, onset, non-stationary voiced or stationary voiced, and the SMV rate may be Rate 1, Rate ½, Rate ¼, or Rate 1/8. The SMV frame classification, using EVRC parameters, is performed according to a pre-defined configuration and classifier algorithm. The coefficients or rules of the classifier are determined during a prior EVRC-to-SMV classifier training or construction process. The frame classification and rate determination module includes a final decision module, that enforces all SMV rate transition rules to ensure illegal rate transitions are not allowed. For example, in SMV, a Rate 1 Type 1 cannot follow a Rate 1/8 frame. This frame classification and rate determination module replaces the SMV standard classifier, which requires a large amount of processing to derive the parameters and features required for classification. The SMV frame-processing functions are shown in Figure 7, and the many steps of the SMV classification procedure are shown in Figure 8. These functions are not necessary in the present invention as the already available EVRC CELP parameters are used as inputs to classifier module.

[0057] The intermediate parameters interpolation module and the SMV smart frame classification and rate determination module are linked to one of many interpolation and mapping modules by a switching module. EVRC has a single processing algorithm for each rate, whereas SMV has two possible processing algorithms for each of Rate 1 and Rate ½, and a single processing algorithm for each of Rate ¼ and Rate 1/8. The SMV frame type and bit rate determined by the frame classification and rate determination module control which interpolation and mapping module is to be chosen. For Rates 1 and ½ of SMV, the stationary

14

voiced frame class uses subframe processing Type 1 and all other frame classes use subframe processing Type 0. As shown in Figure 17, there are interpolation and mapping modules for each allowed EVRC rate and SMV type and rate combination. For example, interpolation and mapping modules include:

5   EVRC Rate 1 to SMV Rate 1 Type 0

           EVRC Rate 1 to SMV Rate 1 Type 1

           EVRC Rate ½ to SMV Rate 1 Type 0

           EVRC Rate ½ to SMV Rate 1 Type 1

           EVRC Rate ½ to SMV Rate ½ Type 0

10   EVRC Rate ½ to SMV Rate ½ Type 1

           ....

and so on.

[0058]   For the EVRC-to-SMV transcoder, interpolation and mapping modules include:

           SMV Rate 1 Type 0 to EVRC Rate 1

15   SMV Rate 1 Type 1 to EVRC Rate 1

           SMV Rate 1 Type 0 to EVRC Rate ½

           SMV Rate 1 Type 1 to EVRC Rate ½

           SMV Rate ½ Type 0 to EVRC Rate ½

           SMV Rate ½ Type 1 to EVRC Rate ½

20   ....

and so on.

[0059]   Each mapping module comprises a speech spectral parameter mapping unit, an excitation mapping unit, and a mapping strategy decision unit. The speech spectral parameter mapping unit maps the EVRC line spectral frequencies directly to SMV line spectral
25   frequencies. This occurs for all source EVRC bit rates. The parameters passed to the excitation mapping unit depend on the source EVRC bit rate. For EVRC Rates 1 and ½, the input CELP excitation parameters are the pitch lag, delta pitch lag (Rate 1 only), adaptive

15

codebook gain, fixed codevectors, and fixed codebook gain. For EVRC Rate 1/8, typically inactive frames, the input excitation parameter is the frame energy. The excitation parameters are mapped to SMV excitation parameters, depending on the selected mapping module and mapping strategy. The mapping strategy decision module controls the mapping

5   strategy to be used. In this example, the mapping strategy for active speech is to perform analysis in the excitation domain.

[0060] Using the EVRC excitation parameters of pitch delay, delta pitch delay, adaptive codebook gain, fixed codevectors, fixed codebook gains and frame energy, the excitation signal is reconstructed. To reduce complexity and quality degradations, the EVRC decoder

10   operations of filtering the excitation signal by the synthesis filter to convert to the speech domain and post-filtering are not used. Similarly, the pre-processing operations of SMV are not used. These include silence enhancement, high-pass filtering, noise suppression and adaptive tilt filtering. Since the EVRC encoder contains noise-suppression operations, the transcoder does not include further noise-suppression functions.

15   [0061] In RCELP-based coders like EVRC and SMV, a fundamental part of the signal processing is in the modification of the speech to match an interpolated pitch track. This saves quantisation bits required for pitch representation, but involves a large amount of computation as pitch pulses must be detected and individually shifted or time-warped. For the EVRC-to-SMV transcoding example, the signal modification functions within the SMV

20   encoder may be bypassed. This is due to the fact that similar signal modification has already been performed in the EVRC encoder. Hence the reconstructed excitation signal already possesses a smooth pitch characteristic and is already in a form amenable to efficient quantization. The target signal for the adaptive codebook search is thus the excitation signal, without pitch modifications, that has been calibrated to account for differences between the

25   quantized EVRC LSFs and the quantized SMV LSFs.

[0062] Mapping of excitation parameters is performed as described in the previous section. Simplifications can be made to the fixed codebook search, as SMV contains multiple sub-codebooks for each rate and frame type. Since the EVRC bit rate, fixed codevector and fixed codebook structure are known, it may not be necessary to search all sub-codebooks to best

30   match target excitation. Instead, each mapping module may contain a single fixed sub-codebook or a subset of the fixed sub-codebooks to reduce computational complexity.

[0063]   A second-stage switching module links the interpolation and mapping module to the SMV bitstream packing module. The bitstream is packed according to the SMV frame type and bit rate. One SMV output frame is produced for each EVRC input frame.

OTHER CELP TRANSCODERS

[0064]   The invention of method and apparatus for voice transcoding between variable rate coders described in this document is generic to all linear prediction-based voice codecs, and applies to any voice transcoders between the existing codecs G.723.1, GSM-AMR, EVRC, G.728, G.729, G.729A, QCELP, MPEG-4 CELP, SMV, AMR-WB, VMR and all other future voice codecs. The invention applies especially to those transcoders, in which the destination coder makes use of rate determination and/or frame classification information.

[0065]   The previous description of the preferred embodiment is provided to enable any person skilled in the art to make or use the present invention. The various modifications to these embodiments will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other embodiments without the use of the inventive faculty. Thus, the present invention is not intended to be limited to the embodiments shown herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein.